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**STATEMENT UNDER 37 CFR 3.73(b)**

Applicant/Patent Owner: Renesas Electronics Corporation & Hitachi ULSI Systems Co., Ltd.  
Application No./Patent No.: 09/987,978 / 5,637,913 Filed/Issue Date: 11/16/01 / 6/10/97  
Entitled: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF  
AND PROCESS FOR FABRICATING THE TWO  
Renesas Electronics Corporation Corporation  
Hitachi ULSI Systems Co., Ltd. Corporation  
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest  
(The extent (by percentage) of its ownership interest is \_\_\_\_\_ %)

in the patent application/patent identified above by virtue of either:

- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy therefore is attached.

OR

- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Yujiro Kaihara; Kazunari Suzuki; Kunihiro Tsubosaki; Hiromichi Suzuki; Yoshinori Miyaki; Takahiro Naito; Suetō Kawai  
To: Hitachi, Ltd. and Hitachi Microcomputer System, Ltd.

The document was recorded in the United States Patent and Trademark Office at  
Reel 007070, Frame 0577, or for which a copy thereof is attached.  
Hitachi, Ltd.

2. From: Hitachi Microcomputer System, Ltd. To: Hitachi ULSI Systems Co., Ltd.  
The document was recorded in the United States Patent and Trademark Office at  
Reel 010828, Frame 0905, or for which a copy thereof is attached.  
Hitachi, Ltd. Renesas Technology Corp.

3. From: Hitachi ULSI Systems Co., Ltd. To: Hitachi ULSI Systems Co., Ltd.  
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☒ Additional documents in the chain of title are listed on a supplemental sheet.

☐ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

(NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08)

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

4. From: Renesas Technology Corp.  
Hitachi ULSI Systems Co., Ltd.

To: Renesas Electronics Corporation  
Hitachi ULSI Systems Co., Ltd.


The document was recorded in the United States Patent and Trademark  
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Reel: 025204 Frame 0512



Renesas Electronics Corporation  
By: Hironori SEKI  
Title: General Manager  
Intellectual Property Division

10/3/11  
Date



Hitachi ULSI Systems Co., Ltd.  
By: Shiro BABA  
Title: President & CEO

Nov. 15, 2011  
Date